



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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03.20.02

Applicant: Ronnie I. Chaiken and David M. Gillies  
Serial No.: 09/994,258      Group Art Unit: 2183  
Filed: November 26, 2001      Docket: 50037.70US01  
Due Date: February 26, 2002  
Title: METHOD FOR BINARY-LEVEL BRANCH REVERSAL ON COMPUTER ARCHITECTURES SUPPORTING PREDICATED EXECUTION

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the paper(s), as described herein, are being deposited in the United States Postal Service, as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on February 21, 2002.

By: 

John E. Whitaker

Commissioner for Patents  
Washington, D.C. 20231

Sir:

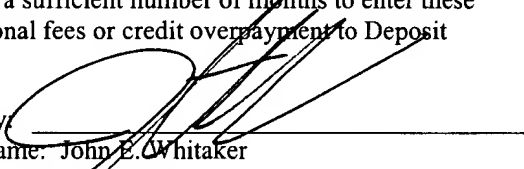
We are transmitting herewith the attached:

- ☒ Transmittal Sheet in duplicate containing Certificate of Mailing
- ☒ Information Disclosure Statement, Form 1449, 8 Reference(s)
- ☒ Return postcard

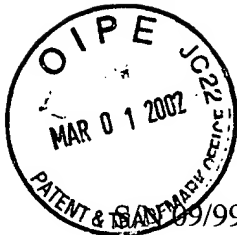
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Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers or any future reply, if appropriate. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

MERCHANT & GOULD P.C.  
P.O. Box 2903, Minneapolis, MN 55402-0903  
206.342.6200

By:   
Name: John E. Whitaker  
Reg. No.: 42,222  
JEW/LW





09/994,258

PATENT

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INFORMATION DISCLOSURE STATEMENT (37 C.F.R. § 1.97)

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Commissioner for Patents  
Washington, D.C. 20231

MAR 06 2002

Technology Center 2100

Dear Sir:

With regard to the above-identified application, the items of information listed on the enclosed Form 1449 are brought to the attention of the Examiner.

This statement should be considered because it is submitted within three months of the filing date of the above-identified application, which is not an application under 37 C.F.R. § 1.53(d). Accordingly, no fee is due for consideration of the items listed on the enclosed Form 1449.

In accordance with 37 C.F.R. § 1.98(a)(2), a copy of each document or other information listed on the enclosed Form 1449 is provided.

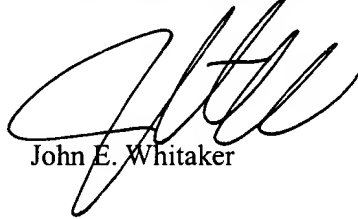
No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103 and Applicants reserve the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish that the reference(s) are not "prior art." Moreover, Applicants do not represent that a reference has been thoroughly reviewed or that any relevance of any portion of a reference is intended.

Consideration of the items listed is respectfully requested. Pursuant to the provisions of M.P.E.P. 609, it is requested that the Examiner return a copy of the attached Form 1449, marked as being considered and initialed by the Examiner, to the undersigned with the next official communication.

Please charge any additional fees or credit any overpayment to Deposit Account No. 13-2725.

Respectfully submitted,

MERCHANT & GOULD



John E. Whitaker

JEW/JLW:cwk



<b>FORM 1449*</b> <b>INFORMATION DISCLOSURE STATEMENT</b>  <b>IN AN APPLICATION</b> (Use several sheets if necessary)	Docket Number: 50037.70US01	Application Number: 09/994,258
	Applicant: Ronnie I. Chaiken and David M. Gillies	
	Filing Date: November 26, 2001	Group Art Unit: 2183

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,937,195	08/10/1999	Ju et al.	395	709	

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FOREIGN PATENT DOCUMENTS							
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
5/1991	On Predicated Execution, HPL-91-58, May, 1991, Joseph C. H. Park, Mike Schlansker, pp. 1-25.	
1996	Global Predicate Analysis and its Application to Register Allocation, IEEE 1996, 1072-4451/96, David M. Gillies, Dz-ching Roy Ju, Richard Johnson, and Michael Schlansker, pp. 114-125.	
8/17/1998	Swizzle barrier optimizations for orthogonal persistence in Java, presented at the Third International workshop on Persistence and Java, August 17, 1998, pp. 1-14.	
1999	Static Single Assignment Form for Machine Code, SIGPLAN '99 (PLDI) 5/99, 1999 ACM 1-58113-083-X/99/0004, Allen Leung and Lal George, pp. 204-214.	
2000	Dynamic Optimization Infrastructure and Algorithms for IA-64, Kim Michelle Hazelwood, These presented to North Carolina State University, Master of Science, pp.1-98.	
2000	Accurate and Efficient Predicate Analysis with Binary Decision Diagrams, IEEE 2000, 0-7695-0924, John W. Sias, Wen-mei W. Hwu and David I. August, pp. 112-123.	
9/2000	IA64 Software Conventions and Runtime Architecture, Document Number 245358-002, September 2000, pp 11-1 to 11-18. See <a href="http://developer.intel.com">http://developer.intel.com</a>	



EXAMINER	DATE CONSIDERED
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.	